# Lab 01 – Worksheet

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| Name: | ID: | Section: |

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| **Note:** Assumptions and logics should be explained separately in tasks after the task results. |

## Task 1a.

*Provide appropriately commented codes (Mention question part before each part)*

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*Add screenshot of your results (register)*

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## Task 1b.

*Provide appropriately commented codes (Mention question part before each part)*

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*Add screenshot of your results (register)*

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## Task 2a

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| Write the RISC-V assembly code for each item below. Try guessing the result in each destination before executing the instruction and corroborate it after execution:   * Store x10 as unsigned integer at address 0x100.  |  | | --- | |  |  * Store x11 as unsigned integer at address 0x1F0.  |  | | --- | |  |  * Load an unsigned short integer (two bytes) from address 0x100 in x12.  |  | | --- | |  |  * Load a short integer from address 0x1F0 in register x13.  |  | | --- | |  |  * Load a singed character from address 0x1F0 in register 0x13.  |  | | --- | |  | |

*Add screenshots of your results (register & memory both)*

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**Task 2b**

*Provide appropriately commented codes (Mention question part before each part)*

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*Add screenshots of your results (register & memory both)*

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**Exercise**

*1. Provide appropriately commented codes (Mention question part before each part)*

*Add screenshots of your results (register & memory both)*

*2. Provide appropriately commented codes (Mention question part before each part)*

*Add screenshots of your results (register & memory both)*

# Lab 01

# Assessment Rubrics

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| --- | --- | --- | --- |
| **Task No.** | **LR 2**  **Code** | **LR 5**  **Results** | **AR 7**  **Report Submission/Code Comments** |
| **Task 1a** | /05 | /05 | /20 |
| **Task 1b** | /10 | /05 |
| **Task 2a** | /10 | /05 |
| **Task 2b** | /20 | /05 |
| **Exercise** | /10 | /05 |  |
| **Total Points** |  | /100 Points |  |
| **CLO Mapped** |  | CLO 2 |  |

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| **#** | **Assessment Elements** | **Level 1: Unsatisfactory** | **Level 2: Developing** | **Level 3: Good** | **Level 4: Exemplary** |
| **LR2** | **Program/Co de/ Simulation Model/ Network Model** | Program/code/simulation model/network model does not implement the required functionality and has several errors. The student is not able to utilize even the basic tools of the software. | Program/code/simulation model/network model has some errors and does not produce completely accurate results. Student has limited command on the basic tools of the software. | Program/code/simulation model/network model gives correct output but not efficiently implemented or implemented by computationally complex routine. | Program/code/simulation  /network model is efficiently implemented and gives correct output. Student has full command on the basic tools of the software. |
| **LR5** | **Results & Plots** | Figures/ graphs / tables are not developed or are poorly constructed with erroneous results. Titles, captions, units are not mentioned.  Data is presented in an obscure manner. | Figures, graphs and tables are drawn but contain errors. Titles, captions, units are not accurate. Data presentation is not too clear. | All figures, graphs, tables are correctly drawn but contain minor errors or some of the details are missing. | Figures / graphs / tables are correctly drawn and appropriate titles/captions and proper units are mentioned. Data presentation is systematic. |
| **AR9** | **Report Content/Code Comments** | No summary provided. The number/amount of tasks completed below the level of satisfaction and/or submitted late | Couldn’t provide good summary of in-lab tasks. Some major tasks were completed but not explained well.  Submission on time. Some major plots and figures provided | Good summary of In-lab tasks. All major tasks completed except few minor ones. The work is supported by some decent explanations, Submission on time, All necessary plots, and figures provided | Outstanding Summary of In-Lab tasks. All task completed and explained well, submitted on time, good presentation of plots and figure with proper label, titles and captions |